

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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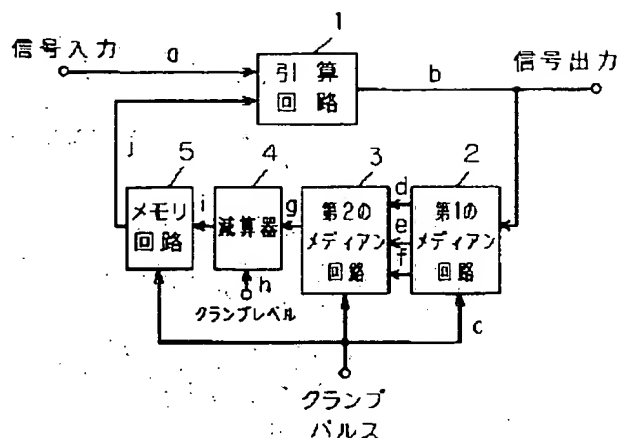
APPLICATION DATE : 25-11-92  
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APPLICANT : MATSUSHITA ELECTRIC IND CO LTD;

INVENTOR : KITADA KATSUHISA;

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TITLE : CLAMPING CIRCUIT



**ABSTRACT :** **PURPOSE:** To provide a clamping circuit capable of stably clamping a black level to a reference level even for video signals in which pulse noise is mixed concerning the clamping circuit of the video signals.

**CONSTITUTION:** A difference between an input signal level and a feedback signal level is obtained by a subtracting circuit 1, and the output signals of the subtracting circuit 1 are sampled by clamp pulses for the prescribed period of time at cascade connected median circuits 2 and 3. The sampled signals are converted to a median value by plural median filters and a subtractor 4 subtracts a clamp level from the median value and outputs a control value. A memory circuit 5 holds the previous control value until such time as sampling is performed by the next clamp pulse and the next control value is obtained and then stably clamps the black level of the video signals to the reference level.

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